

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Not for submission under 37 CFR 1.99)	Application Number		10596249	
	Filing Date		2006-06-06	
	First Named Inventor	Buehrer, et al.		
	Art Unit	2042- 2891		
	Examiner Name	Sarkar		
Attorney Docket Number		FIS920030120US1		

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/Asok Sarkar/

03/31/2009

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	2	HORI T.; "A 0.1-Mum CMOS Technology With Tilt-Implanted Punchthrough Stopper (TIPS)" - Institute Of Electrical And Electronics Engineers - Technical Digest Of The International Electron Devices Meeting - IEEE, US, (Dec 1994), vol. Meeting 40 - pp. 75-78.	<input type="checkbox"/>

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